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APPLICATION NO.	1	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/888,395		06/26/2001	Kuo-Liang Tsai	TSAI3013/EM/6924	9474
23364	7590	05/19/2004		EXAMI	NER
BACON &		-	MICHALSKI	MICHALSKI, JUSTIN I	
FOURTH F			ART UNIT	PAPER NUMBER	
ALEXAND	ALEXANDRIA, VA 22314			2644	2
				DATE MAILED: 05/19/2004	

Please find below and/or attached an Office communication concerning this application or proceeding.

	Application No.	Applicant(s)					
	09/888,395	TSAI, KUO-LIANG					
Office Action Summary	Examiner	Art Unit					
	Justin Michalski	2644					
The MAILING DATE of this communication Period for Reply	appears on the cover sheet	with the correspondence address					
A SHORTENED STATUTORY PERIOD FOR RE THE MAILING DATE OF THIS COMMUNICATIO - Extensions of time may be available under the provisions of 37 CFR after SIX (6) MONTHS from the mailing date of this communication. - If the period for reply specified above is less than thirty (30) days, a - If NO period for reply is specified above, the maximum statutory per - Failure to reply within the set or extended period for reply will, by sta Any reply received by the Office later than three months after the may earned patent term adjustment. See 37 CFR 1.704(b).	N. R 1.136(a). In no event, however, may reply within the statutory minimum of the did will apply and will expire SIX (6) Matute, cause the application to become	a reply be timely filed thirty (30) days will be considered timely. ONTHS from the mailing date of this communication. ABANDONED (35 U.S.C. § 133).					
Status							
1)⊠ Responsive to communication(s) filed on 20	6 June 2001						
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· <u>-</u>	,						
•	closed in accordance with the practice under Ex parte Quayle, 1935 C.D. 11, 453 O.G. 213.						
Disposition of Claims		•					
4) ☐ Claim(s) 1-6 is/are pending in the application 4a) Of the above claim(s) is/are without 5) ☐ Claim(s) is/are allowed. 6) ☐ Claim(s) 1-6 is/are rejected. 7) ☐ Claim(s) is/are objected to. 8) ☐ Claim(s) are subject to restriction and	drawn from consideration.						
Application Papers							
9) The specification is objected to by the Exam	niner.						
10) The drawing(s) filed on is/are: a) a	0)☐ The drawing(s) filed on is/are: a)☐ accepted or b)☐ objected to by the Examiner.						
Applicant may not request that any objection to t	the drawing(s) be held in abey	/ance. See 37 CFR 1.85(a).					
Replacement drawing sheet(s) including the con	•						
Priority under 35 U.S.C. § 119							
12) Acknowledgment is made of a claim for fore a) All b) Some * c) None of: 1. Certified copies of the priority docume 2. Certified copies of the priority docume 3. Copies of the certified copies of the papplication from the International Bur * See the attached detailed Office action for a	ents have been received. ents have been received in priority documents have been reau (PCT Rule 17.2(a)).	Application No en received in this National Stage					
Attachment(s)							
1) Notice of References Cited (PTO-892)	4) Interview	w Summary (PTO-413)					
Notice of Draftsperson's Patent Drawing Review (PTO-948) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/Paper No(s)/Mail Date		lo(s)/Mail Date of Informal Patent Application (PTO-152) 					

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DETAILED ACTION

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35
 U.S.C. 102 that form the basis for the rejections under this section made in this
 Office action:

A person shall be entitled to a patent unless -

- (b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.
- 2. Claims 1 and 3-6 are rejected under 35 U.S.C. 102(b) as being anticipated by Op De Beek et al. (Hereinafter "Op De Beek") (US Patent 4,628,530).

Regarding Claim 1, Op De Beek discloses in a system a process for automatically performing a frequency response equalization tuning, said process comprising the steps of: installing a speaker (Figure 1, speaker 11) in an electronic device (Figure 1); commanding a central processing unit (CPU) (references 16, and 9) in said electronic device to simulate and generate a standard sound signal (signal from reference 5 to 11) having a predetermined bandwidth (Op de Beek discloses the audible frequency range of 20Hz to 20kHz) (Column 2, line 28) through a sound control circuit in said electronic device; commanding a digital equalizer (reference 9) in said electronic device to receive said standard sound signal (signal from reference 5 to 11) and outputting said standard sound signal through a speaker (11) in said electronic device; commanding a microphone (14) to receive said standard sound signal for sending back to said CPU (16 and 9) through said sound control circuit (Figure 1); commanding said CPU to perform a frequency response matching on said

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received sound signal with respect to a predetermined ideal frequency response data (frequency analyzer 17 and Column 1, lines 48-54); calculating a set of equalization tuning gains in said predetermined bandwidth (Column 2, lines 49-52); inputting said gains in said digital equalizer for storing (signal 10); and commanding said digital equalizer to automatically perform a frequency response equalization tuning on said speaker, thereby maintaining said output sound signal at an optimum frequency response state (Op de Beek discloses automatic correction (equalization) of acoustic signal (Column 1, lines 34-35).

Regarding Claim 3, Op de Beek further discloses after said digital equalizer (9) has received said sound signal from said sound control circuit, further comprising the steps of commanding said digital equalizer (9) to compensate said sound signal based on a predetermined initial gain (signal 10), and sending said compensated sound signal to said speaker (11) for outputting.

Regarding Claim 4, Op de Beek further discloses after said microphone (14) has received said compensated sound signal, further comprising said steps of commanding said sound control circuit to convert said sound signal into a digital sound data (converter 18), and sending said digital sound data back to said CPU (16).

Regarding Claim 5, Op de Beek further discloses commanding said CPU to perform a frequency response matching on said received sound signal with respect to said outputted standard sound signal based on a plurality of sampling frequencies (Op de Beek discloses frequency bands of 40Hz) (Column 2, lines

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49-52); and inputting said gains in said digital equalizer for storing (reference 9 via signal 10).

Regarding Claim 6, Op de Beek further discloses said standard sound signal having said predetermined bandwidth is a standard bandwidth is a standard sound file in an audible frequency range of 20Hz and 20kHz (Column 2, line 51).

Claim Rejections - 35 USC § 103

- 3. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
 - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- 4. Claim 2 is rejected under 35 U.S.C. 103(a) as being unpatentable over Op de Beek as applied to claim 1 above. Op de Beek discloses a process as stated apropos of claim 1 above. Op de Beek further discloses said electronic device is manufactured to have said predetermined ideal frequency response data with said predetermined bandwidth based on specifications of said speaker (Op de Beek discloses correction (equalization) of signal radiated by a transducer unit (i.e. specifications of speaker) (Column 1 lines 33-39), to be installed in said electronic device prior to storing said ideal frequency response data in a memory of said electronic device so that when said electronic device is enabled a predetermined software in said CPU reads said ideal frequency response data

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from said memory for generating said standard sound signal (signal from 5 to 11) having said predetermined bandwidth (i.e. flat frequency response) through said sound control circuit (Although Op de Beek does not explicitly disclose software in CPU (16 and 17) one skilled in the art would have known that frequency analyzer which operates on digital signals from units 25 and 18 would inherently have some sort of software or program to control the processes of units 16 and 9).

Conclusion

5. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

Janse et al. (US Patent 5,768,398) discloses Figure 2 showing an automatic equalizer with feedback microphone.

Miller et al. (US Patent 5,506,910) discloses an automatic equalizer with desired frequency response.

6. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Justin Michalski whose telephone number is (703)305-5598. The examiner can normally be reached on 8 Hours, 5 day/week.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Bill Isen can be reached on (703)305-4386. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

JIM

PRIMARY EXAMINE